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Applicant	Kouji MATSUO			
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U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
	6,376,888	04/23/02	TSUNASHIMA et al.			
	2003-0143825 A1	07/31/03	MATSUO et al.			

FOREIGN PATENT DOCUMENTS						
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
	2000-195966	07/14/00	JAPAN			ABSTRACT
	2002-270797	09/20/02	JAPAN			ABSTRACT
	2002-530864	09/17/02	JAPAN			NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	MATSUO, K., "Semiconductor Device and Method of Manufacturing the Same", U.S. Application No. 10/396,453, filed March 26, 2003.

Examiner	Date Considered 1/18/05
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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